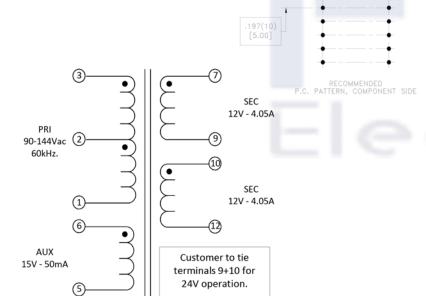


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ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	7-9	@20°C	0.010 ohms max.
D.C. RESISTANCE	10-12	@20°C	0.010 ohms max.
D.C. RESISTANCE	6-5	@20°C	0.044 ohms ±20%
D.C. RESISTANCE	3-1	@20°C	0.165 ohms ±10%
INDUCTANCE	3-1	10kHz, 100mV, Ls	180μH ±10%
SATURATION CURRENT	3-1	20% rolloff from initial	7.3A
LEAKAGE INDUCTANCE	3-1	tie(5+6, 7+9, 10+12),100kHz, 100mV, Ls	6.0μH max.
DIELECTRIC	1-12	tie(3+5, 9+10), 3750VAC, 1 second	
TURNS RATIO		(3-1):(6-5)	10:1, ±1%
TURNS RATIO		(3-1):(7-9)	10:1, ±1%
TURNS RATIO		(3-1):(10-12)	10:1, ±1%
TURNS RATIO		(3-2):(2-1)	1:1, ±1%

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40 °C to +125 °C including temp rise. Designed to comply with the following requirements as defined by UL840 and UL5085:

- Reinforced insulation for a primary circuit at a working voltage of 160Vrms, Pollution Degree 2

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

DFM DRAWING TITLE PART NO. SP Packaging Specifications Tolerances unless otherwise specified: Angles: ±1° DATE 11/19/2014 Method: Tray Decimals: ±.005 [.13] **TRANSFORMER** FKS750813743 PKG-0872 Fractions: ±1/64 Footprint: ± .001 [.03] ENG JJB REV. 01 This drawing is dual dimensioned. Dimensions in brackets are in millimeters. DATE 2/17/2015 SPECIFICATION SHEET 1 OF 1