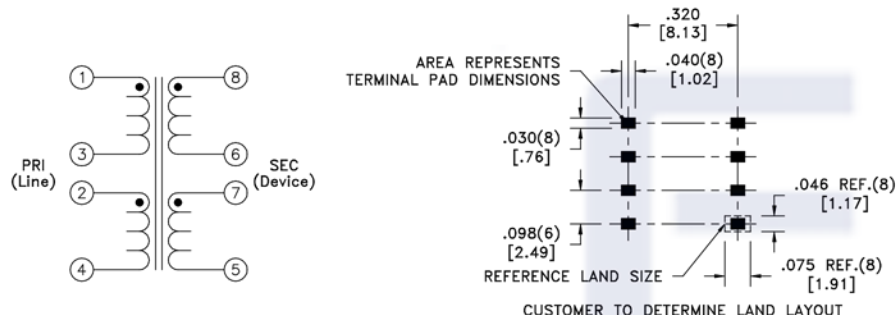
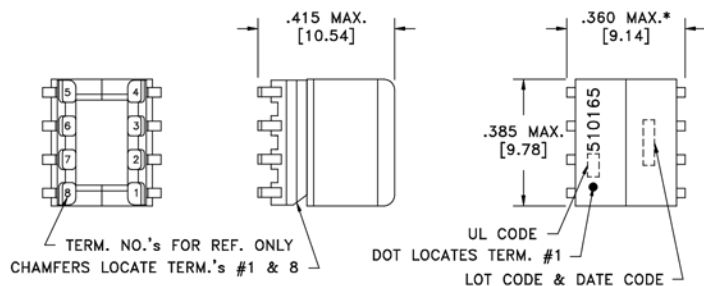


CUSTOMER TERMINAL	RoHS	LEAD(Pb)-FREE
Sn96%, Ag4%	Yes	Yes



* DIMENSION MAY BE EXCEEDED WITH SOLDER ONLY



ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER	TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-3 @20°C	0.500 ohms max.
D.C. RESISTANCE	2-4 @20°C	0.500 ohms max.
D.C. RESISTANCE	8-6 @20°C	0.250 ohms max.
D.C. RESISTANCE	7-5 @20°C	0.250 ohms max.
INDUCTANCE	1-4 tie(2+3), 10kHz, 100mVAC, Ls	1.20mH ±10%
LEAKAGE INDUCTANCE	1-4 tie(2+3, 6+7, 5+8), 1MHz, 100mVAC, Ls	3uH max.
INTERWINDING CAPACITANCE	1-8 tie(2+3, 6+7), 100kHz, 100mVAC, Cs	60pF max.
DIELECTRIC	1-8 tie(2+3, 6+7), 1875VAC, 1 second	1500VAC, 1 minute
URNS RATIO	(1-4):(8-5), tie(2+3, 6+7)	3:1, ±1.5%
TOTAL HARMONIC DISTORTION	40kHz, 2Vrms across PRI, 11 ohm load, 100 ohm input, tie(2+3, 6+7)	-80dB max.
LONGITUDINAL BALANCE ¹	20k - 2MHz	50dB min.

¹Per ITU Method (L->M).

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +85°C.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:
- Supplementary insulation for a primary circuit at a working voltage of 250Vrms.

DETAILS SUBJECT TO CHANGE

AGENCY NUMBER

UL CODE
FILE# E205930 BC

REV.	DATE	Packaging Specifications
		Method: Tape & Reel PKG-0410
6B	12/08	
6A	10/08	



Tolerances unless otherwise specified:
Angles: ±1° Decimals: ±.005 [.13]
Fractions: ±1/64 Footprint: ±.005 [.13]

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

TRANSFORMER

eiSos p/n: FKS750510165



PART NO.

FKS750510165

SPECIFICATION SHEET 1 OF 1