





Customer to tie terminals 1&2&3 and 4&5&6 on PC board.

Application of the transformer allows for the leadwires between terminals 1&2&3 and 4&5&6 to solder bridge.



ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	9-12	@20°C	0.800 ohms ±10%
D.C. RESISTANCE	6-1	tie(6+5+4, 3+2+1), @20°C	0.010 ohms max.
D.C. RESISTANCE	7-8	@20°C	0.282 ohms ±10%
D.C. RESISTANCE	13-14	@20°C	0.125 ohms ±10%
INDUCTANCE	9-12	10kHz, 100mVAC, Ls	520.00uH ±5%
LEAKAGE INDUCTANCE	9-12	tie(1+2+3+4+5+6+7+8+13+14), 90kHz, 100mVAC, Ls	12uH typ., 16uH max.
DIELECTRIC	9-6	tie(9+7+13, 6+5+4), 3500VAC, 1 second	3500VAC, 1 minute
DIELECTRIC	9-8	tie(7+14), 1250VAC, 1 second	1000VAC, 1 minute
TURNS RATIO		(9-12):(6-1), tie(6+5+4, 3+2+1)	12:1, ±1%
TURNS RATIO		(9-12):(7-8)	6:1, ±1%
TURNS RATIO		(9-12):(13-14) 24:1, ±2%	

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

 Basic insulation for a primary circuit at a working voltage of 265Vrms, 400Vpeak.

Designed to comply with the following requirements as defined by IEC61558-2-16, Amendment 1: EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Reinforced insulation for a primary circuit at a working voltage of 190Vrms.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

	REV.	DATE	Packaging Spe
			Method: Tray
1			PKG-0736
ı			
ı	64	1/16	

AUX (Nb)

5.2V, 100mA

S CONVENTION PLACEMEN

Tolerances unless otherwise specified:
Angles: ±1*

Perimals: ±.005 [.13]

Fractions: ±1/64

Pootprint: ±.001 [.03]

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

TRANSFORMER

FKS750370156

eiSos p/n: FKS750370156

