

.197

[5.00]

Customer to tie terminals 8+9 and 12+13 on PC board.

AUX

15V, 5mA

AUX

10V, 50mA



## ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-3	@20°C	1.42 ohms ±10%
D.C. RESISTANCE	4-5	@20°C	0.625 ohms ±10%
D.C. RESISTANCE	5-6	@20°C	0.530 ohms ±10%
D.C. RESISTANCE	12-9	tie(8+9, 12+13), @20°C	0.029 ohms ±20%
INDUCTANCE	1-3	10kHz, 100mVAC, Ls	1.20mH ±10%
SATURATION CURRENT	1-3	20% rolloff from initial	625mA
LEAKAGE INDUCTANCE	1-3	tie(8+9+12+13), 100kHz, 100mVAC, Ls	32uH typ., 45uH max.
DIELECTRIC	1-13	tie(3+4, 8+9), 4600VAC, 1 second	4600VAC, 1 minute
DIELECTRIC	1-6	625VAC, 1 second	_
TURNS RATIO		(1-2):(2-3)	1:1, ±1%
TURNS RATIO		(1-3):(4-5)	7:1, ±1%
TURNS RATIO		(1-3):(5-6) 10:1, ±1%	
TURNS RATIO		(1-3):(12-9), tie(8+9, 12+13) 8.75:1, ±1%	

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC61558-2-16 and EN61558-2-16:

Reinforced insulation for a primary circuit at a working voltage of 425Vrms, 600Vpeak (operating frequency of <2MHz).

Designed to comply with the following requirements as defined by IEC60335-1: - Reinforced insulation for a primary circuit at a working voltage of 425Vrms, 600Vpeak.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Speci
		Method: Tray
		PKG-0736
C 4	0/17	



Tolerances unless otherwise specified: Decimals: ±.005 [.13] Footprint: ±.001 [.03] Angles: ±1\* Fractions: ±1/64

RECOMMENDED

P.C. PATTERN, COMPONENT SIDE

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

## TRANSFORMER

FKS7508110347

eiSos p/n: FKS7508110347

