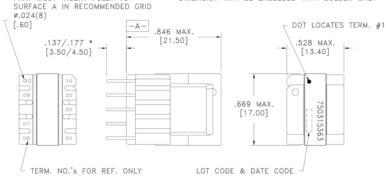
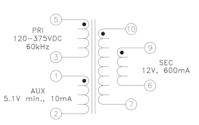


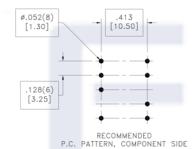
PART MUST INSERT FULLY TO

* DIMENSION MAY BE EXCEEDED WITH SOLDER ONLY





Customer to tie terminals 6&7 and 9&10 on PC board.





ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	5-3	@20°C	4.75 ohms ±10%
D.C. RESISTANCE	1-2	@20°C	1.55 ohms ±10%
D.C. RESISTANCE	10-7	tie(6+7, 9+10), @20°C	0.170 ohms ±10%
INDUCTANCE	5-3	10kHz, 100mVAC, Ls	2.00mH ±10%
SATURATION CURRENT	5-3	20% rolloff from initial	625mA
LEAKAGE INDUCTANCE	5-3	tie(1+2, 6+7+9+10), 100kHz, 100mVAC, Ls	100uH typ., 150uH max.
DIELECTRIC	1-10	tie(2+3), 3750VAC, 1 second	3000VAC, 1 minute
DIELECTRIC	1-5	625VAC, 1 second	_
TURNS RATIO		(5-3):(1-2)	10:1, ±1%
TURNS RATIO		(5-3):(10-7), tie(6+7, 9+10)	5.86:1, ±1%

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Reinforced insulation for a primary circuit at a working voltage of 265Vrms, 400Vpeak, Overvoltage Category II.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Spe
		Method: Tray
		PKG-1030
C A	10/10	



Tolerances unless otherwise specified: Decimals: ±.005 [.13] Footprint: ±.001 [.03] Angles: ±1* Fractions: ±1/64

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

TRANSFORMER

FKS750315363

eiSos p/n: FKS750315363

