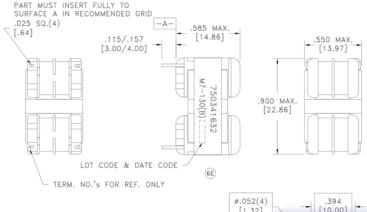
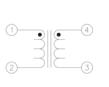
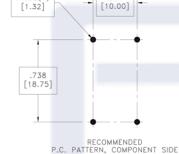
LEAD(Pb)-FREE Sn96%, Ag4% Yes Yes









## © ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-2	@20°C	0.19 ohms max.
D.C. RESISTANCE	4-3	@20°C	0.19 ohms max.
INDUCTANCE	1-2	10kHz, 50mVAC, Ls	3.3mH +60%/-30%
INDUCTANCE 4-3 SATURATION CURRENT 1-4		10kHz, 50mVAC, Ls	3.3mH +60%/-30%
		tie(2+3), roll off 20% from initial	5.0A
LEAKAGE INDUCTANCE	1-4	tie(2+3), 10kHz, 50mVAC, Ls	90.0uH min.
DIELECTRIC	1-Core	tie(1+4), 1875VAC, 1 second	1500VAC, 1 minute
DIELECTRIC	1-4	2514VAC, 1 second	2011VAC, 1 minute
TURNS RATIO		(1-2):(4-3)	1:1, ±2%

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to meet UL Recognized Insulation System M7-130(B), E106391.

- 6D Current Rating: 2.0A
- 6F Designed to comply with UL1993 for 347VACrms or VDC.
- Designed to comply with UL840 for 347VACrms or VDC for Overvoltage  $\ensuremath{\mathfrak{GF}}$  Category II and Pollution Degree II.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Spe
6F	1/16	Method: Tray
6E	10/14	PKG-0822
6D	11/13	
6C	10/13	

Tolerances unless otherwise specified: Angles: ±1° Decimals: ±.005 [.13] Footprint: ±.001 [.03] Fractions: ±1/64

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

CMC

FKS750341632

eiSos p/n: FKS750341632